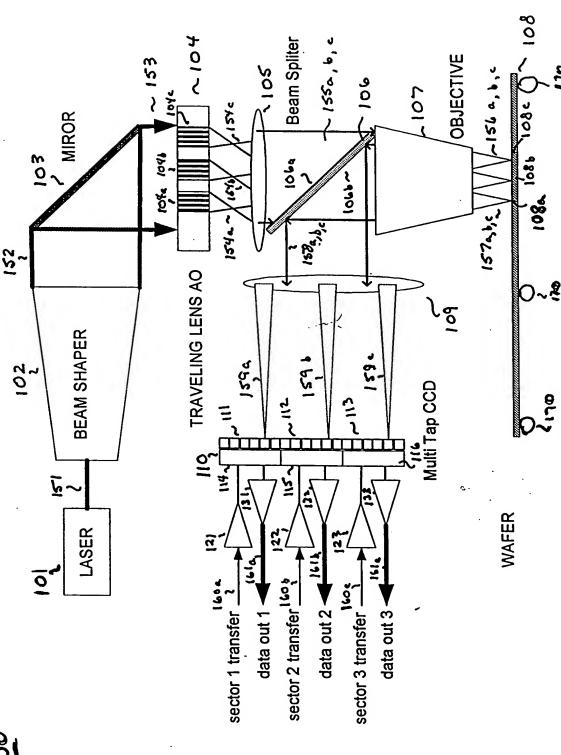
Figure 1: System Schematic diagram



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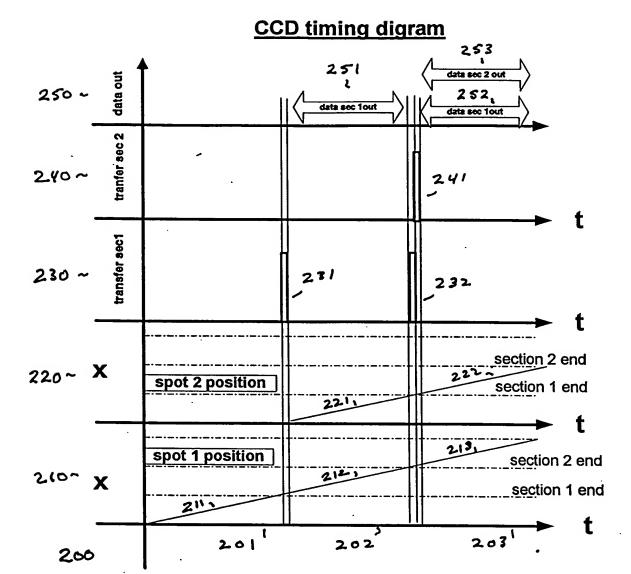
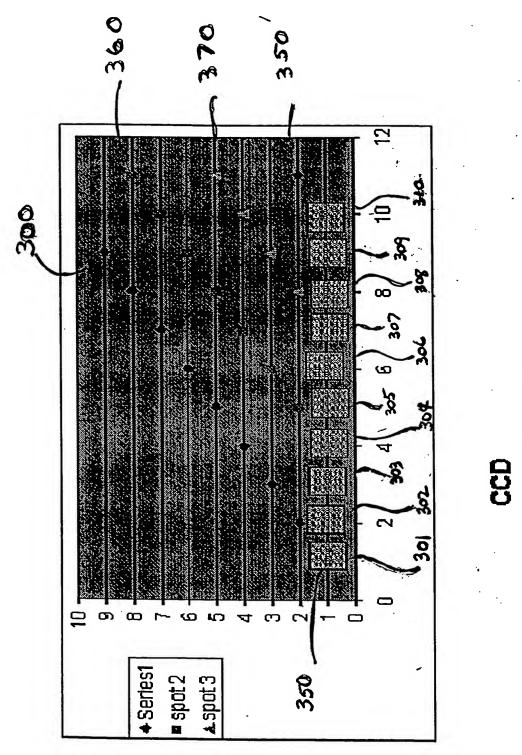
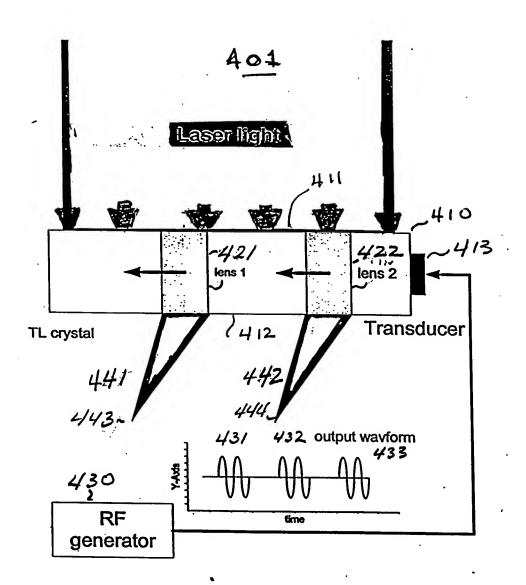


Figure 2: Timing diagram



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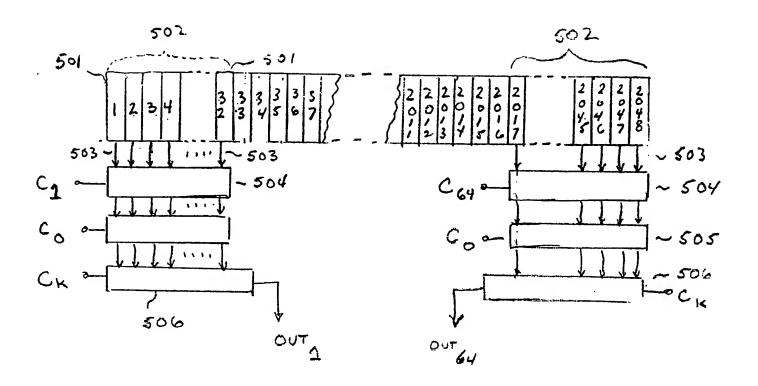
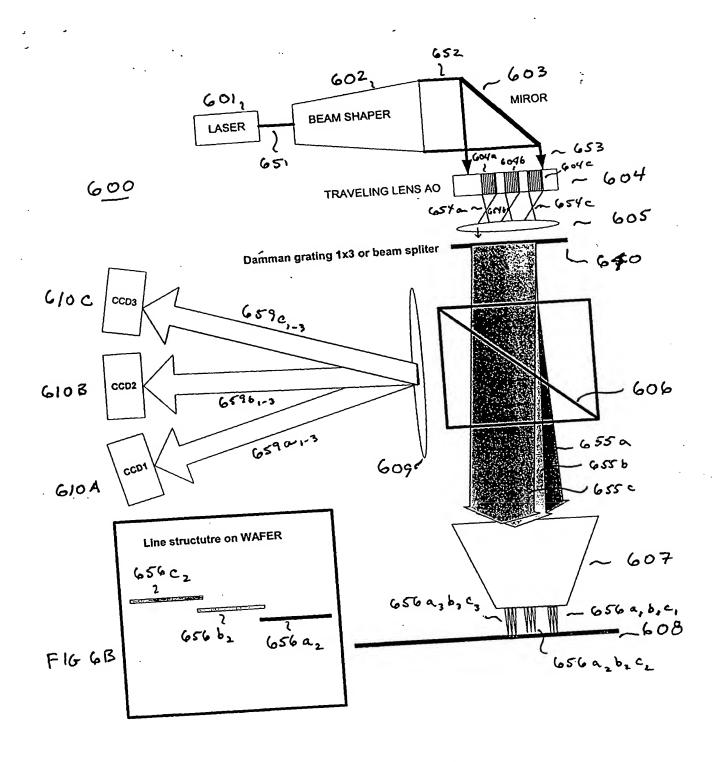
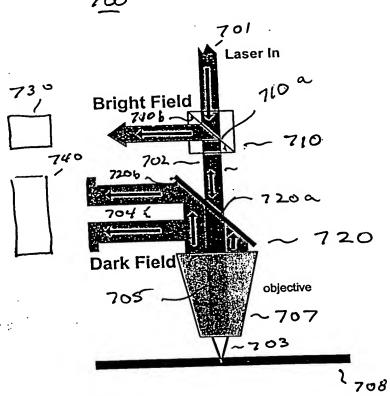


FIGURE 5

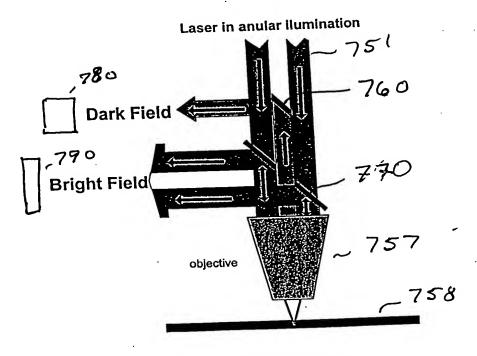


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